

Notice of References Cited	Application/Control No. 10/721,331	Applicant(s)/Patent Under Reexamination WADA, KENJI	
	Examiner Heather A. Doty	Art Unit 2813	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,843,821	12-1998	Tseng, Horng-Huei	438/253
	C	US-6,720,641	04-2004	Birdsley et al.	257/621
	D	US-4,016,593	04-1977	Konishi et al.	257/116
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf and R.N. Tauber, Silicon Processing for the VLSI Era, v. 1, 2nd edition, Lattice Press, 2000, pg. 784.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.